



Effect of grain size on the energy storage properties of $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ paraelectric ceramics

Zhe Song^a, Hanxing Liu^{a,*}, Shujun Zhang^b, Zhijian Wang^a, Yatong Shi^a, Hua Hao^a, Minghe Cao^a, Zhonghua Yao^a, Zhiyong Yu^a

^a State Key Laboratory of Advanced Technology for Materials Synthesis and Processing, Wuhan University of Technology, Wuhan 430070, Hubei, PR China

^b Materials Research Institute, Pennsylvania State University, University Park, PA 16802, USA

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Abstract

$(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ (BST) ceramics with various grain sizes (0.5–5.6 μm) were prepared by conventional solid state reaction methods. The effect of grain size on the energy storage properties of BST ceramics ($T_c \approx -65^\circ\text{C}$) was investigated. With decreasing grain sizes, a clear tendency toward the diffuse phase transition was observed and the dielectric nonlinearity was reduced gradually, which can be explained by the Devonshire's phenomenological theory (from the viewpoint of intrinsic polarization). Based on the multi-polarization mechanism model, the relationship between the polarization behavior of polar nano-regions (the extrinsic nonlinear polarization mechanisms) and grain size was studied. The variation of the grain boundary density was thought to play an important role on the improvement of dielectric breakdown strength, account for the enhanced energy density, which was confirmed by the complex impedance spectroscopy analysis based on a double-layered dielectric model.

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Keywords: Paraelectric BST; Grain size; Dielectric nonlinearity; Energy storage property; The grain boundary density

1. Introduction

Recently, $(\text{Ba}_{1-x}\text{Sr}_x)\text{TiO}_3$ ($x=0\text{--}1$) (BST) has received considerable attentions due to the potential applications in the field of electric energy storage, by the virtue of their high power density and good reliability.¹ The Curie point (T_c) of $(\text{Ba}_{1-x}\text{Sr}_x)\text{TiO}_3$ can be controlled by varying the mole fraction of Sr, accompanied by significant variation of dielectric properties, which can be tailored for specific applications.²

With the continuous miniaturization of components, size-dependent properties have been of great interest in many types of electronic ceramics including BST. Grain size effects on the phase evolution, microstructure and dielectric properties of BaTiO_3 -based ceramics have been reported since 1950s.^{3–5} Previous reports on the properties of BST ceramics for the potential energy storage applications showed a strong dependent on the grain sizes, where the large variation of dielectric constant,⁶ the decrease of dielectric nonlinearity⁷ as well as the

improvement of dielectric breakdown strength⁸ were confirmed to be associated with the refinement of grain size.

Based on the observation of grain size effect in BaTiO_3 -based ceramics, many mechanisms have been proposed, such as internal stress,⁴ 90° ferroelectric domains⁵ and the role of grain boundary layer,⁹ etc. However, most of the publications about the grain size effect and its intrinsic mechanisms were focused on materials with ferroelectric phase,^{10,11} other than paraelectric phase, which was considered more potential for electric energy storage dielectrics.¹² So it is interesting to discuss the grain size effect for BST ceramics with paraelectric phase, to improve the energy storage performance by the refinement of grain sizes and establish the relationship between specific intrinsic mechanisms (such as the grain boundary density, other than the role of ferroelectric domains) and the macroscopic properties.

In this study, $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ was chosen as the research object, which show paraelectric phase structure at room temperature ($T_c \approx -65^\circ\text{C}$) and relatively low dielectric loss ($\text{tg } \delta \approx 0.005$), moderate dielectric constant (~ 1000), and high dielectric breakdown strength ($\sim 150 \text{ kV/cm}$) as well as good dielectric stability under dc bias field,¹³ being potential for electric energy storage applications.¹⁴ Following these

* Corresponding author. Tel.: +86 27 87653330; fax: +86 27 87885813.

E-mail address: lhxhp@whut.edu.cn (H. Liu).

considerations, a comprehensive study of the grain size effect on the energy storage properties of $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ with paraelectric phase and its corresponding mechanisms were investigated.

2. Experimental

The $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ polycrystalline ceramics were prepared by the conventional solid state reaction method with high purity (99.95%) commercial powders of BaCO_3 , SrCO_3 and TiO_2 . The stoichiometric mixtures were ball milled with zirconium media in ethanol for 24 h. After drying, the powders were calcined at 1180°C for 4 h in air and then pressed to form disk-shaped samples at 150 MPa. To prepare dense ceramics with various grain sizes, the green pellets were sintered at different temperatures (1260 – 1400°C) for 2 h. Finally, the sintered ceramics were polished to 0.3 mm in thickness and coated with fire-on silver electrodes for electrical measurements.

Density measurement was carried out using the Archimedes method. XRD analysis was performed on the sintered pellets using Cu K_α radiation (X'Pert PRO, PANalytical, Holland). Microstructure and grain size were observed on thermally etched surface of the sintered samples by field-emission scanning electron microscope (FE-SEM) (S-4800, Hitachi, Japan). Average grain sizes of BST were statistically calculated by using Image Pro plus software, which were simulated by the average length of diameters passing through objects' centroid, measured at 2 degree intervals.

The dielectric nonlinearity was measured by a precision impedance analyzer (Agilent 4980 A, Agilent, USA) connected with a dc power supply. The dielectric breakdown strength and P - E hysteresis loop were examined at room temperature using a Radiant precision workstation (Radiant RT66A) based on the Sawyer-Tower circuit at 10 Hz. The energy storage density γ was evaluated by integrating the area between the polarization axis and the discharge curve in the P - E hysteresis loop, which is given by the following equation:

$$\gamma = \int_0^{E_b} E \, dP \quad (1)$$

where P and E are the polarization and the electric field, E_b is the breakdown electric field.¹⁵

3. Results and discussion

3.1. Phase characterization and microstructure observation

XRD patterns of the BST ceramics with different sintering temperatures are shown in Fig. 1. The phase structure was found to maintain the same at different sintering temperatures, where no obvious second phase was observed. For all samples, the split of (2 0 0) peaks corresponding to the tetragonality was not observed, revealing that all samples exhibit paraelectric phase with cubic perovskite structure.

Fig. 2 shows the FE-SEM images of BST ceramics with various grain sizes obtained at different sintering temperatures. For better observation, the sintered samples were polished about 300 μm off and then thermal etched 100°C below their

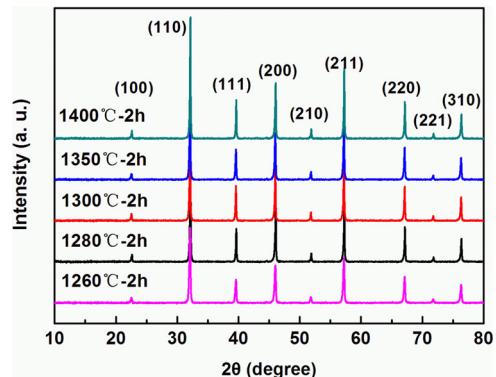


Fig. 1. XRD patterns of BST ceramics under different sintering conditions.

respective sintering temperatures. As observed in the figures, all samples show uniform and homogeneous morphologies, while the grain size was found to strongly depend on the sintering temperature. After the calculation on SEM images by Image Pro Plus software, it was found that with increasing sintering temperatures from 1260°C to 1400°C , the average grain size changed from $0.5 \mu\text{m}$ to $5.6 \mu\text{m}$, which can be regarded as an ideal range at micro scale to illustrate the grain size dependent properties effectively.⁵

Fig. 3 gives the measured densities for the sintered samples, which were found to be above 96% of the theoretical value, regardless the sintering temperature. In this case, the variation of densities has minimal impact on the properties of BST with different grain sizes,¹⁶ thus the change of grain size is considered as the dominant factor affecting the macroscopic properties.

The temperature-dependent dielectric properties for BST ceramics with different grain sizes are given in Fig. 4, which were measured at 10 kHz frequency. For all the samples, the dielectric constants are about 1500 with dielectric losses being less than 0.005 at room temperature, the Curie temperatures (T_c) were found to locate at around -65°C , indicating the room temperature paraelectric phase, which is in good agreement with the XRD results (shown in Fig. 1). As the grain size reduced from $5.6 \mu\text{m}$ to $0.5 \mu\text{m}$, a significant decrease of dielectric constant around the Curie temperature was observed, with a clear tendency toward a diffuse phase transition, which was thought to be inherently associated with the internal stress between grains.^{17,18}

3.2. Grain size effect on the dielectric nonlinearity and the nonlinearity mechanisms

The dielectric nonlinearity represents the nonlinear decrease of the dielectric constant as a function of applied voltage, which will not benefit the energy storage density. It has long been recognized that this decrease of dielectric constant with electric field is an intrinsic property of ferroelectric ceramics, while for ceramics with paraelectric phase, the nonlinearity is much lower.¹⁹

3.2.1. Grain size effect on the dielectric nonlinearity

Fig. 5 exhibits the field dependence of the dielectric constants at 10 kHz for BST ceramics, with the external dc bias field

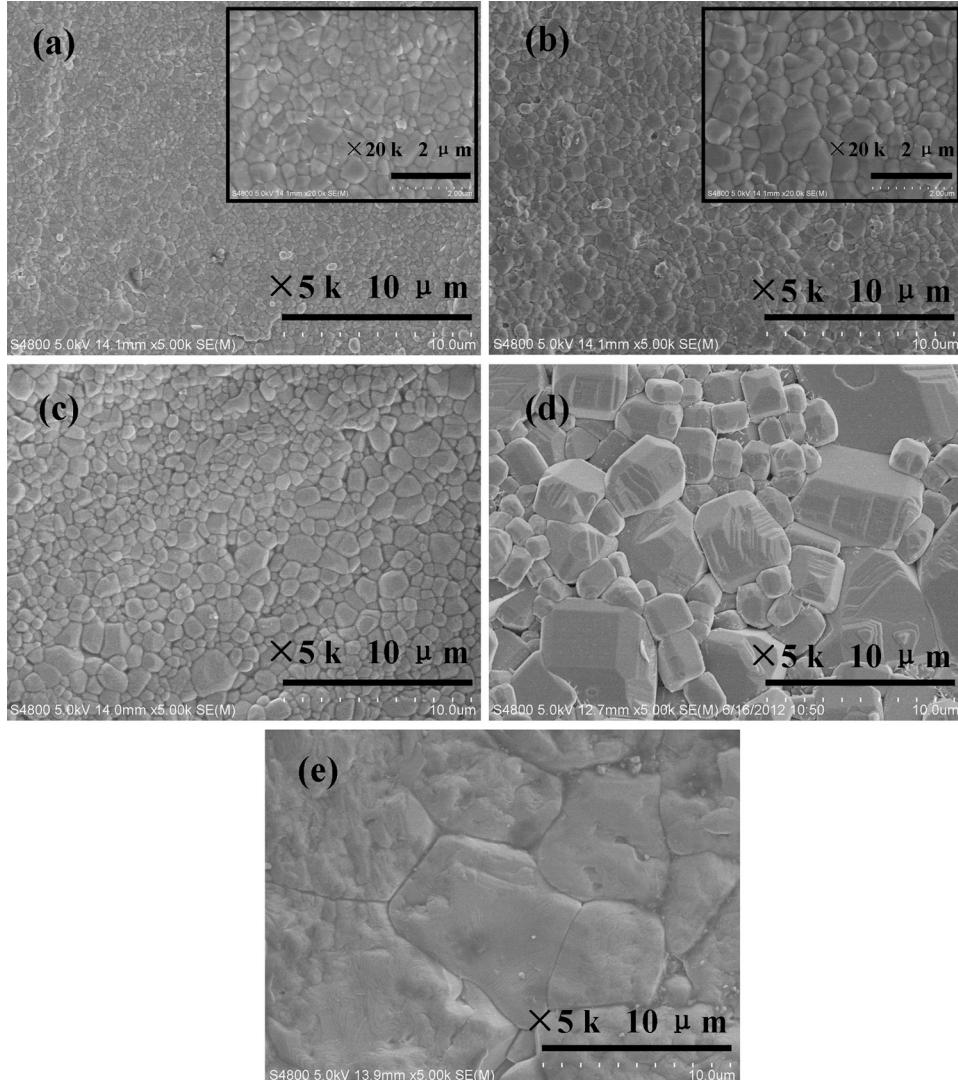


Fig. 2. SEM images on the surface of BST ceramics with different sintering temperatures as well as grain sizes: (a) 1260 °C–2 h, GS (grain size)=0.5 μm; (b) 1280 °C–2 h, GS=0.7 μm; (c) 1300 °C–2 h, GS=1.2 μm; (d) 1350 °C–2 h, GS=2.2 μm; and (e) 1400 °C–2 h, GS=5.6 μm.

varying in the range of 0 kV/cm to 20 kV/cm. It was found that the dielectric constant increased to \sim 1180 gradually with increasing grain size in a certain range of 0.5–2.2 μm, as predicted by the “series and parallel combination” model.⁶ However, the dielectric constant of BST ceramic with the grain size

of 5.6 μm is lower than that of sample with grain size of 2.2 μm, which may due to the over-sintering at 1400 °C.

For all samples, the dielectric constants tend to be reduced slightly with increasing bias electric field, showing weaker dielectric nonlinearity even in paraelectric phase. This can be explained by the Devonshire’s phenomenological theory²⁰ where this phenomenon is attributed to the suppression of the anharmonic interaction of Ti⁴⁺ ions in Ti–O octahedrons.

To characterize the dielectric nonlinearity, the dielectric tunability k is defined at a specific dc bias electric field:

$$k = \frac{\varepsilon_r(0) - \varepsilon_r(E)}{\varepsilon_r(0)} \times 100\% \quad (2)$$

where $\varepsilon_r(0)$ and $\varepsilon_r(E)$ are the dielectric constants under zero bias field and applied bias field E , respectively.

As listed in Table 1, when the applied bias field is up to 20 kV/cm, compared with ferroelectric BaTiO₃,²¹ the dielectric tunability of (Ba_{0.4}Sr_{0.6})TiO₃ is much lower, being <5% and the tunability is reduced gradually with decreasing grain sizes.

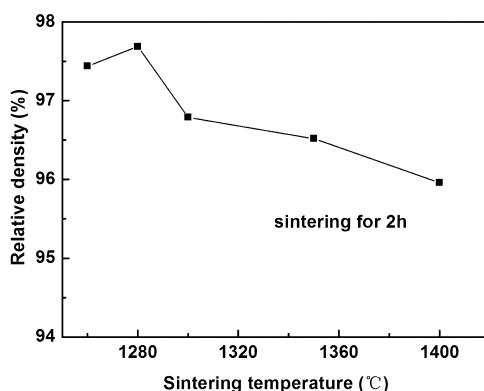


Fig. 3. Relative density of BST ceramics sintering at different temperatures.

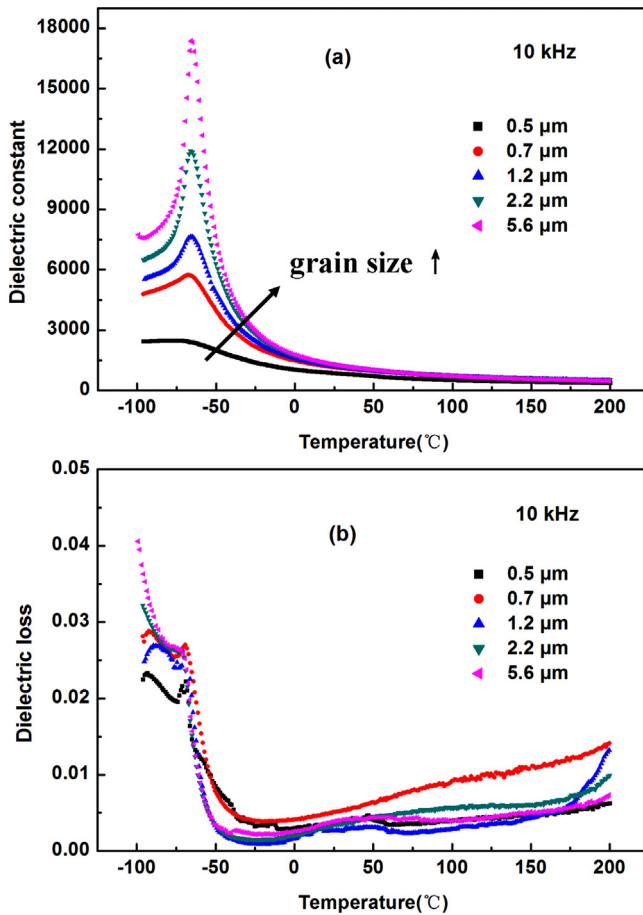


Fig. 4. Dielectric constant (a) and dielectric loss (b) versus temperature for $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ dense ceramics with different grain sizes at the frequency of 10 kHz.

Dielectric ceramics can be regarded as a composite including ferroelectric grain cores with high dielectric constant and insulated grain boundary layers with low dielectric constant. Under the bias field, the grain boundary remains linear and makes no contribution to the dc-tunability. As the grain size decreases, the

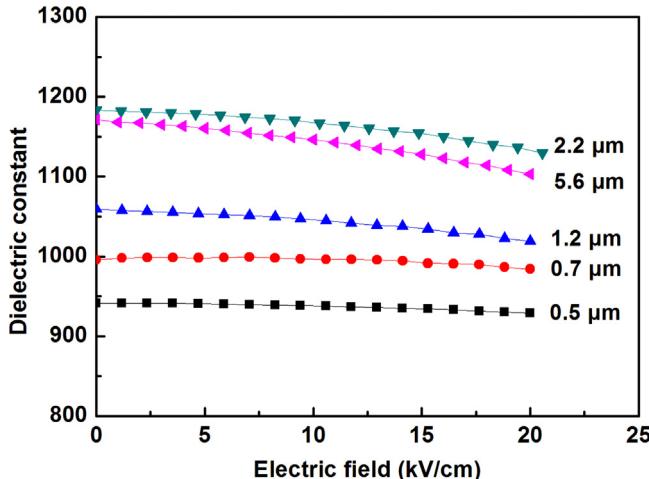


Fig. 5. The dielectric constant as a function of bias electric field for BST ceramics with different grain sizes.

Table 1

Dielectric tunability and slope of fitting lines in Fig. 6 for BST ceramics with various grain sizes.

Samples	Grain size (μm)	Dielectric tunabilities (%)	Slopes of fitting lines (kV/cm) $^{-2}$
1#	0.5	1.14	0.99×10^{-2}
2#	0.7	1.27	1.01×10^{-2}
3#	1.2	3.83	2.93×10^{-2}
4#	2.2	4.28	3.38×10^{-2}
5#	5.6	5.85	4.69×10^{-2}

volume fraction of the grain boundary (i.e. the grain boundary density) increases, thus the “dilution” effect on the dielectric nonlinearity becomes more evident, which can explain the depressed dielectric tunabilities with decreasing grain size.²²

3.2.2. Grain size effect on the nonlinearity mechanisms

The nonlinear variation of ε_r for BST paraelectric ceramics under bias electric field could be fitted to the Johnson's phenomenological expression based on the Devonshire's phenomenological theory, as described by the following²³:

$$\frac{\varepsilon_r(E)}{\varepsilon_r(0)} = \frac{1}{[1 + \alpha \varepsilon_r^3(0) E^2]^{1/3}} \quad (3)$$

wherein α represents the anharmonic coefficient, corresponding to the levels of anharmonic interactions among Ti^{4+} ions.

The fitting results of the dielectric nonlinearity of BST ceramics with various grain sizes according to the Johnson's expression are shown in Fig. 6. The $(\varepsilon_r(0)/\varepsilon_r(E))^3$ vs. E^2 plots of the BST samples show linear behaviors, which are in good agreement with the Johnson's expression, indicating the dielectric nonlinearity of BST paraelectric ceramics is dominated by the suppression of the anharmonic interaction of Ti^{4+} ions according to the Devonshire's phenomenological theory. The fitting slopes (representing $\alpha \varepsilon_r^3(0)$ in Eq. (3)) were found to increase gradually with the grain size increasing, being consistent with the variation of the dielectric tunability, as listed in Table 1.

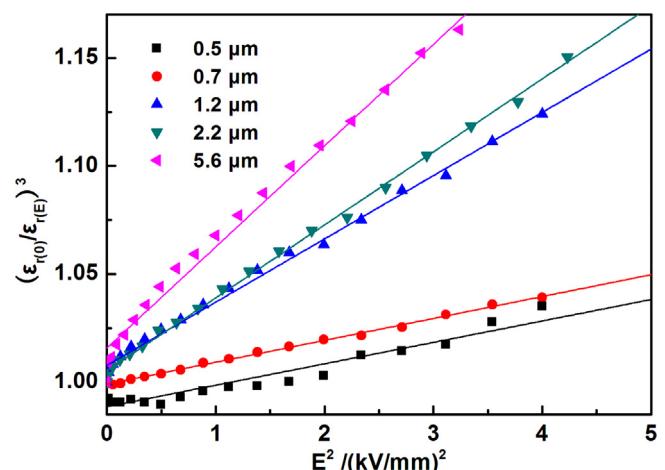


Fig. 6. The fitting results of dielectric nonlinearity of BST ceramics with various grain sizes according to Johnson formula.

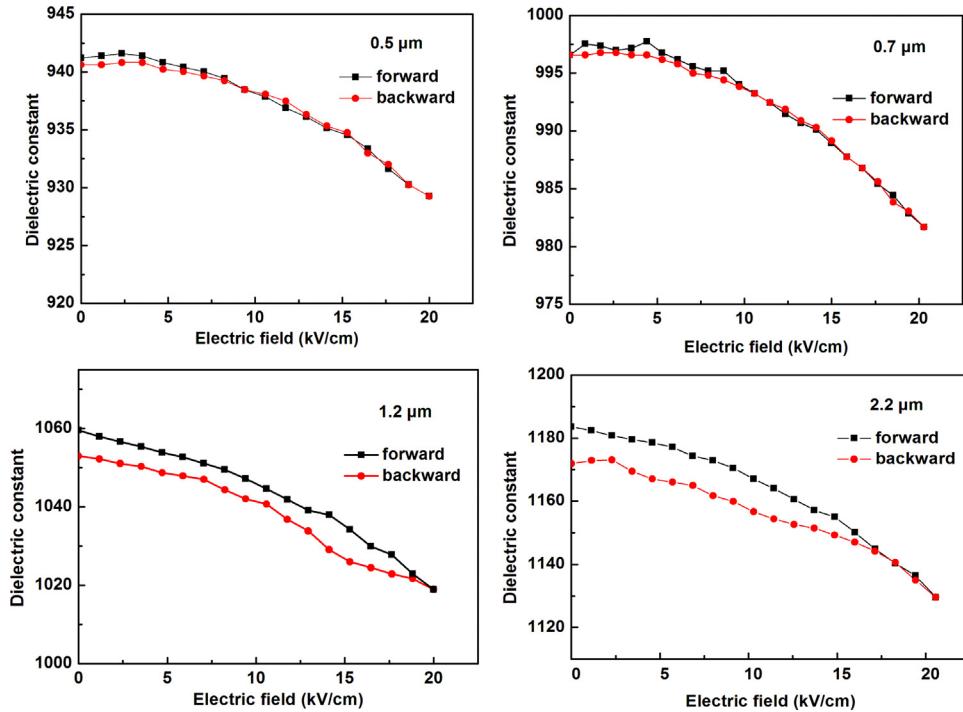


Fig. 7. Bias electric field circuits for BST ceramics with various grain sizes.

The ε_r vs. E circuits for field-increasing (the forward direction) and field-decreasing (the backward direction) of BST ceramics with various grain sizes are comparatively presented in Fig. 7. Generally, the dielectric constants in field-increasing curves are higher than that in field-decreasing curves due to the response of polar nano-regions (PNRs) under bias electric field.²⁴ During the field-increasing process, the polarization reorientation and arrangement of PNRs occurs, while the field-induced behavior is partially frozen under the field-decreasing conditions. As a result, the polarization contribution of PNRs to the dielectric constant is depressed and the deviation phenomenon occurs between the field-increasing and field-decreasing processes. Here, PNRs generally refer to nano-sized polar clusters and are strongly dependent on the grain sizes, which were observed in paraelectric BST induced by the local polar movement of Ti^{4+} ions.²⁵

As shown in Fig. 7, the field-increasing curves were found to overlap with the field-decreasing curves for samples with smaller grain sizes of $0.5\text{ }\mu\text{m}$ and $0.7\text{ }\mu\text{m}$. The deviation degree between the forward and backward directions was found to increase with increasing grain sizes, which was thought to be mainly induced by the variation of PNRs' polarization response under bias fields.

In order to characterize the polarization contribution of PNRs to paraelectric BST with different grain sizes, the dielectric constants under bias field were fitted to the multi-polarization mechanism model, which is derived from the Johnson's phenomenological expression,²⁶

$$\varepsilon_{r(E)} = \frac{\varepsilon_{r(0)}}{[1 + \alpha(\varepsilon_0 \varepsilon_{r(0)})^3 E^2]^{1/3}} + \sum \left(\frac{P_0 x}{\varepsilon_0} \right) [\cosh(Ex)]^{-2} \quad (4)$$

in which $x = P_0 V/k_B T$, P_0 and V are the polarization and equivalent volume for extrinsic polarization entities, which are proposed as PNRs. The first term of the equation, i.e. the Johnson's term, refers to the intrinsic polarization mechanism (the anharmonic interactions among Ti^{4+} ions). And the second term, i.e. the PNRs' term, represents the extrinsic contributions of PNRs.

Fig. 8 shows the fitting curves according to the multi-polarization mechanism model for the samples with different grain sizes. The fitting plots are generally coincide with the experimental data, where the polarization response of the intrinsic and extrinsic mechanisms could be separated. As shown in Fig. 8, PNRs' contributions to dielectric constant are relatively low compared with the intrinsic term. For ceramics with larger grains ($1.2\text{ }\mu\text{m}$ and $2.2\text{ }\mu\text{m}$), the sensitivity of PNRs to bias field increases significantly and their contributions to dielectric constant are minimized to zero and remain high stability with the bias field above 5 kV/cm .

The characteristic parameters of PNRs were obtained from the fitting results, as listed in Table 2, in which d is the equivalent size of PNRs, being equal to $V^{1/3}$ in Eq. (4). Of particular significance is that with increasing grain sizes, the polarization of PNRs tends to be reduced and their equivalent sizes increase gradually. For ceramics with grain size of $1.2\text{ }\mu\text{m}$ and $2.2\text{ }\mu\text{m}$, the equivalent size of PNRs expands to 1.37 nm and 4.04 nm ,

Table 2
Characteristics of polar nano-regions in BST ceramics with various grain sizes.

Grain sizes (μm)	0.5	0.7	1.2	2.2
P_0 ($\mu\text{C}/\text{cm}^2$)	6.28	1.54	0.075	0.041
d (nm)	0.19	0.30	1.37	4.04

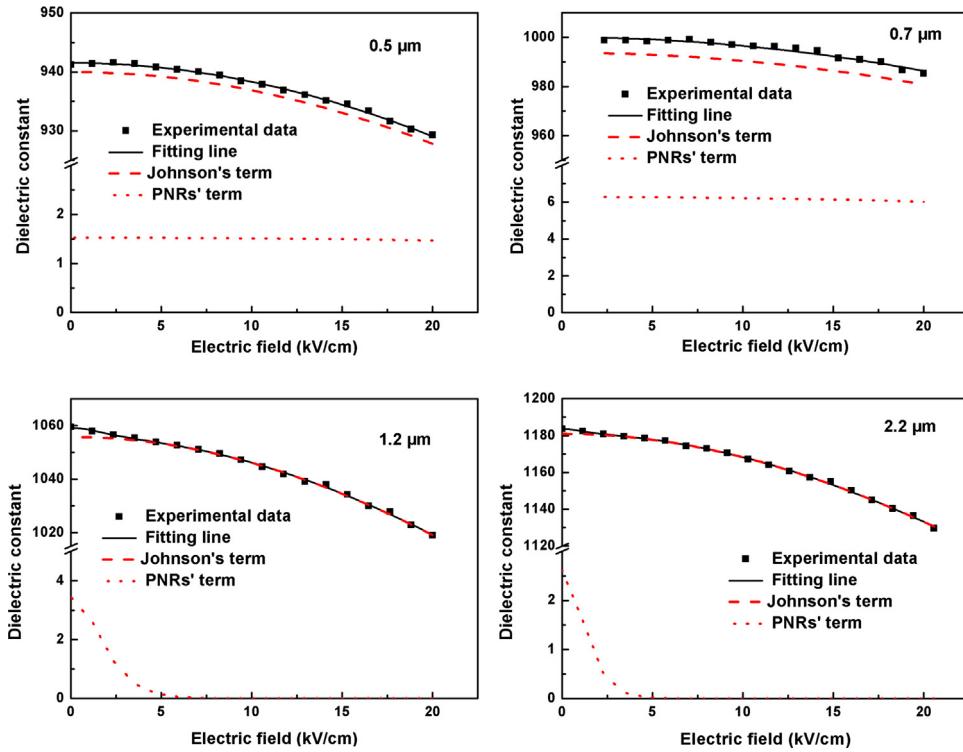


Fig. 8. Fitting curves according to the multi-polarization mechanism model of BST ceramics with various grain sizes.

respectively, revealing that the dielectric response of PNRs is dependent on the grain sizes.

3.3. Grain size effect on energy storage properties

$P-E$ hysteresis loops of $(\text{Ba}_{0.4}\text{Sr}_{0.6})\text{TiO}_3$ with various grain sizes achieved at different applied electric fields prior to dielectric breakdown strength (maximum electric fields) are shown in Fig. 9. The hysteresis characteristic of the samples with various grain sizes is also dependent on the applied electric field, in addition to the materials properties. For sample with 0.5 μm grain size, the broadening of the hysteresis loop is mainly due to the mass accumulation of the space charge at the grain boundary interface under a higher electric field.²⁷ With the decrease of grain size, the dielectric breakdown strength increases, thus the applied electric field can be enhanced greatly, leading to the

enhancement of space charge effect, accompanied by a higher leakage current, as demonstrated by the lossy $P-E$ loop.²⁸ For samples with 2.2 μm and 5.6 μm grain sizes, on the contrary, slim hysteresis loops were obtained due to the lower applied fields.

The energy storage performance parameters of all samples can be evaluated from the hysteresis loops, as listed in Table 3. The energy density γ was calculated by integrating the area between the polarization axis and the discharge curve in the $P-E$ hysteresis loop. With the decrease of grain size, the dielectric breakdown strength (E_b) improves obviously, accompanied by the increase of maximum polarization (P_{max}), which will favor a higher energy density. Sample 1# with the minimum grain size of 0.5 μm exhibits the maximum energy density, being on the order of 1.28 J/cm³ due to its highest dielectric breakdown strength (243 kV/cm) and relatively high maximum polarization (20.6 μC/cm²).

The significant improvement of E_b for samples with smaller grain size was considered to be associated with the increase of the grain boundary density. As discussed in Section 3.2.1, dielectric ceramics can be regarded as a composite including ferroelectric grain cores and insulated grain boundary layers. The volume fraction of the grains and grain boundaries changes with grain size variation, which plays an important role for the dielectric breakdown strength.

3.4. Grain size effect on complex impedance characteristics

To examine the variation tendency about the volume/size ratio of the grain-grain boundary quantitatively, complex impedance

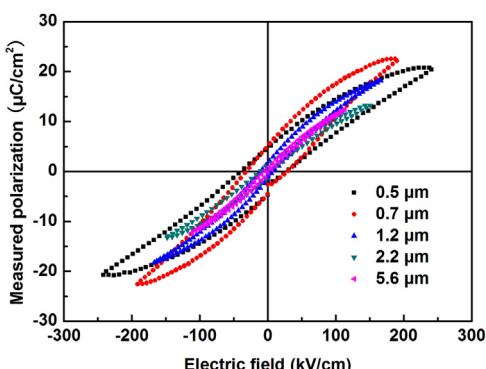


Fig. 9. The hysteresis loops of BST ceramics with various grain sizes.

Table 3

Energy storage properties of BST ceramics with various grain sizes.

Samples	Grain size (μm)	E_b (kV/cm)	Maximum polarization, P_{\max} ($\mu\text{C}/\text{cm}^2$)	Energy density, γ (J/cm 3)
1#	0.5	243	20.6	1.28
2#	0.7	191	22.3	1.20
3#	1.2	167	18.3	1.11
4#	2.2	151	13.2	0.76
5#	5.6	114	12.5	0.60

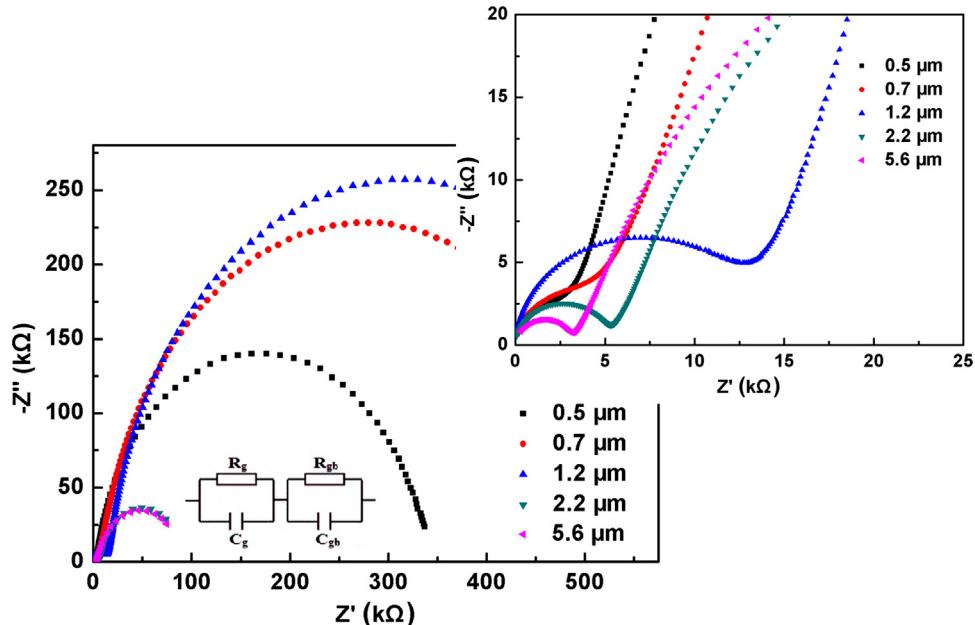


Fig. 10. Complex plane impedance plots of BST ceramics with various grain sizes at 420 °C. Inset 1: the enlarged scale of high frequency area. Inset 2: equivalent circuit proposed for electrical microstructure of BST ceramics.

spectroscopy was used to analyze the electrical microstructure of BST ceramics at the activation temperature of 420 °C and in the frequency range of 20 Hz–2 MHz. As shown in Fig. 10, two semicircular plots with different radii were observed in the complex impedance planes, in which the smaller-sized semicircle corresponds to the response from grain while the larger-scaled one represents the contributions from grain boundary, respectively.

The two semicircular arcs in the impedance spectrum can be simulated by a parallel R - C equivalent circuit,^{29–31} as shown in the inset in Fig. 10. Based on the equivalent circuit, the resistances and capacitances of different electrical regions (grain and grain boundary) can be calculated by using the Z-View software.

The calculated R_g , R_{gb} , C_g and C_{gb} values are given in Table 4. For all samples, the values of R_{gb} are considerably larger than R_g , indicating the better insulation performance of the grain boundaries. The electrical parameters of grain regions as well as grain boundaries were found to change significantly as a function of grain size. Due to the fact that the intrinsic electrical properties (such as the resistivity ρ and the dielectric constant) of the grains and the grain boundaries remain unchanged for BST with varying grain sizes, the changes of R , C values are mainly owing to the variation of the grain size and grain boundary density.

The contribution of the grain boundary density to the grain size effect can be analyzed by a double-layered dielectric

Table 4

Electrical parameters at 420 °C of different electrical regions for BST ceramics with various grain sizes.

Grain size (μm)	R_g (Ω)	R_{gb} (Ω)	R_g/R_{gb}	C_g (pF)	C_{gb} (pF)	C_{gb}/C_g
0.5	4206	304,860	0.01380	137.5	804	5.8516
0.7	6601	467,820	0.01411	136.3	1358	9.9633
1.2	14,498	464,510	0.03121	133.9	7736	57.7745
2.2	5753	69,737	0.08250	148.7	30,839	207.3907
5.6	3558	66,983	0.05318	97.0	21,196	218.5605

model³² based on the R , C electrical parameters. The capacitances of the grains and grain boundaries can be described as:

$$C_g = \frac{\varepsilon_g \cdot A_g}{d_g} \quad (5)$$

$$C_{gb} = \frac{\varepsilon_{gb} \cdot A_{gb}}{d_{gb}} \quad (6)$$

where ε_g and ε_{gb} , A_g and A_{gb} , d_g and d_{gb} represent the dielectric constants, the contact areas, and the mean sizes of the grains and grain boundaries, respectively. In the double-layered dielectric model, it is considered that the grains and grain boundaries share nearly equal contact areas, i.e. $A_g = A_{gb}$. Hence, Eq. (5) and Eq. (6) can be simplified.

$$\frac{C_g}{C_{gb}} = \left(\frac{\varepsilon_g}{\varepsilon_{gb}} \right) \cdot \left(\frac{d_{gb}}{d_g} \right) \quad (7)$$

Similarly, for R_g and R_{gb} the formula can be expressed as:

$$\frac{R_g}{R_{gb}} = \left(\frac{\rho_g}{\rho_{gb}} \right) \cdot \left(\frac{d_g}{d_{gb}} \right) \quad (8)$$

With varying the grain sizes, the resistivity ρ and dielectric constant of the grains and grain boundaries remain unchanged, so do the $\varepsilon_g/\varepsilon_{gb}$ and ρ_g/ρ_{gb} terms. Therefore, it is reasonable that the volume fraction of the grains and grain boundaries (i.e. the variation of d_g/d_{gb}) with the change of grain sizes can be described by the C_{gb}/C_g and R_g/R_{gb} values. It can be seen from Table 4 that with increasing the grain size, the ratios of C_{gb}/C_g and R_g/R_{gb} increase gradually, corresponding to the increase of d_g/d_{gb} , confirming the increase of the volume fraction of ferroelectric grain cores as well as the decrease of insulated grain boundaries, as a function of increasing grain size. It is believed that the variation of the volume fraction of the grains and grain boundaries has an important impact on the grain size effect.

4. Conclusions

In summary, the variation of grain sizes has a significant impact on the energy storage properties of $Ba_{0.4}Sr_{0.6}TiO_3$ paraelectric ceramics. As the grain size reduced from 5.6 μm to 0.5 μm , a clear tendency toward a diffuse phase transition related to the effect of internal stress was observed. With decreasing grain sizes, the dielectric nonlinearity is depressed gradually, driven by the decreasing density of the ferroelectric grains, as well as the increasing of insulated grain boundaries. The grain size-dependent nonlinearity mechanisms (the intrinsic polarization and PNRs) were discussed. The weaker dielectric nonlinearity for $Ba_{0.4}Sr_{0.6}TiO_3$ ceramics can be explained by the Devonshire's phenomenological theory (from the viewpoint of intrinsic polarization), while the polarization behavior of PNRs is believed to make a unique contribution to the nonlinear characteristic of $Ba_{0.4}Sr_{0.6}TiO_3$ ceramics. Based on the multipolarization mechanism model, it was found that with increasing grain size, the polarization of PNRs tends to be reduced and their equivalent sizes increase gradually. And the refinement of grains

will benefit the energy density for BST. The grain boundary density was thought to play an important role on the improvement of dielectric breakdown strength, which was confirmed by the complex impedance spectroscopy analysis based on a double-layered dielectric model.

Conflicts of interest

The authors have no conflicts of interest to declare.

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